

**University of Alaska Fairbanks  
Electrical and Computer Engineering Department  
EE 608 – Power Electronics Design  
Spring 2013**

**Power Electronics Design Project**

**Objective:**

The objective of this project is to design, simulate using PSPICE, build, and test a power electronic conversion device capable of being fed from a single-phase 120VAC source or a 12VDC battery that meets the design requirements listed below. The design project will make use of the knowledge and skills acquired in previous course work and incorporate “***multiple realistic constraints and engineering standards***”. The ***IEEE code of ethics*** will also be addressed in the design process. You will need to combine what you learn in the class lecture and laboratory and additional literature such as technical articles and/or materials from the textbook to complete this project. The laboratory focuses on the design project with six labs during the first half of the semester to address the step-by-step design process and concepts required to build a power electronic conversion device.

**Design Problem Statement (Graduate Credit in Bold Italics Underline):**

You work for WIES Power Electronics Design and your boss has asked you to work on one of the following projects:

- 1) Design a maximum power point tracker (MPPT) system for a 75-W photovoltaic (PV) panel for a ***tracking*** mount arctic region application using remains from the Greenland project. The MPPT PV system will use the available voltage and current from a 75-W PV panel mounted on a ***rotating track*** with a ***specific tilt angle*** to supply maximum available dc power to charge a battery and serve a dc load.
- 2) Design a regulated dc source to power a 1TB ESATA hard drive in a server to store power systems data that is being uploaded from remote monitoring sites in remote communities of Alaska. Each 1TB drive requires a constant 12 Vdc to operate the motor and a constant 5 Vdc to operate the drive electronics with a typical average power consumption model  $P_{typ} = (\text{Idle} * 90\% + \text{Write} * 2.5\% + \text{Read} * 7.5\%) / 100\%$  and a maximum average power consumption model  $P_{max} = (\text{Write} + \text{Seek} + \text{Read} * 3) / 5$  with idle, write, read, and seek power depending on the model. ***The power supply must operate from a single phase 120 Vac +/- 5% at 60 Hz or single phase 250 Vac +/- 5% at 50Hz, supply with unknown supply inductance  $L_s$ . The power supply must provide power to operate two 9 Vdc & 2 Adc cooling fans.***
- 3) A project idea of your own that I must approve via a pre-proposal submitted on Monday, January 28, 2013. It must satisfy the design requirements/standards. Examples include motor controls and drives (Ecocar, EATV, and ESMV) and renewable energy related power conversion equipment.

**Design Requirements:**

- 1) The device must operate from a single-phase 120 VAC source or a 12 VDC battery.
- 2) Simulate the design in PSPICE before construction.
- 3) Construct, test, and verify the circuit on a breadboard. Document all results and demonstrate to the instructor for approval to proceed with CAD, ISO tool and milling.
- 4) Mill the circuit board(s) on the TTech J5 milling machine.
- 5) The proper amount of additional supply inductance must be employed to meet the German VDE standards (inductive reactance is a minimum of 5% of the base impedance).
- 6) A design specific high frequency isolation transformer is required with leakage reactance less than 10% based on its ratings. Cores, forms, clamps, and magnet wire supplies are available to build the transformers.
- 7) The percent voltage ripple at the output of the dc-dc converter must be less than 10%.

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**Design Standards:**

- 1) Follow IEEE Std 519 at point of common coupling for AC supplies:
  - a. IEEE Std 519 establishes harmonic limits on voltage as 5% for total harmonic distortion  $THD_v$  and 3% of the fundamental voltage for any single harmonic.
  - b. IEEE Std 519 establishes the harmonic limits for total harmonic distortion  $THD_i$  and total demand distortion  $TDD_i$  of the AC line current (see attached paper).
- 2) Follow IEEE Std 299-1997: Method for Measuring the Effectiveness of Electromagnetic Shielding Enclosures for proper shielding against electromagnetic EMI and radio frequency RFI interference.

**Specific Graduate Student Requirements:**

- 1) ***You will be working on your own*** following a documented project workplan and timeline submitted with your proposal. The instructor will sign off on each task in the workplan as it is completed. Project performance will be based strictly on design criteria.
- 2) ***Journal quality writing in IEEE technical journal format*** is expected from graduate students for the project proposal, midterm progress, draft, and final reports.
- 3) ***You will be required to take or more in depth look at a specific contemporary issue approved by the instructor related to your power electronic device.*** Examples of contemporary issues in power electronics design include switching harmonics, switch efficiency, voltage ripple, electromagnetic or radio frequency interference, feedback control stability, and isolation of PWM and power switching circuits.

**Reports:**

***You are required*** to write a proposal, three major project reports, and four bi-weekly progress reports as part of the design project.

- 1) **Proposal (Due: Monday, Feb. 6, 2012):** ***You are required*** to write a project proposal which lays out your intended design based on the given requirements. ***You will be required*** to submit a project workplan outlining the tasks for each team member as part of the proposal. You will meet with the instructor to discuss the proposal.
- 2) **Bi-weekly Progress (Due: Jan. 30, Feb. 20, Mar. 5, Mar. 26):** ***You are required*** to write and submit four bi-weekly project progress reports (1-2 pages) which briefly discuss the current status, results, and adherence to the project timeline. If the instructor determines that a team is not making sufficient progress on the design, the instructor will meet with that team to discuss possible avenues for moving forward to successful project completion.
- 3) **Midterm Progress (Due: Monday, Mar. 19, 2012):** ***You are required*** to write a midterm progress report which discusses your progress up to the time of the mid-term progress presentations. You will meet with the instructor to discuss the mid-term progress.
- 4) **Draft Report (Due: Monday, Apr. 9, 2012):** ***You are required*** to write a draft project report explaining your design based on the given requirements. Make sure to include any references and annotate them in order of reference within the report. You will meet with the instructor to discuss edits to the draft report.
- 5) **Final Report (Due: Wednesday, May 9, 2012):** ***You are required*** to complete a final project report explaining your design based on the given requirements and making final conclusions and edits from the draft report. Make sure to include any references and annotate them in order of reference within the report.

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**Report Format:**

The specific written format requirements with section labels for the proposal, three major project reports (midterm, draft & final), and four bi-weekly progress reports will be provided in separate documents. The proposal and three major project reports should be sufficient in length to cover the required sections. The reports should give background on the specific application and explain how it applies to power electronics. You will also need to address a contemporary issue with the device that you are designing and building. Your report should also include any pertinent tables, figures, PSPICE inputs and outputs and CAD schematics for the purpose of demonstrating the application. Please provide a list of numbered references (including websites) in the order referenced in the reports and use the numbers in square brackets to annotate the referenced material in the report. Figures and tables need to have captions. Captions for figures are below the figure, while captions for tables are above the table. Also, please number the pages beginning with the second page. If you have an excessive amount of design and simulation results, you should include it in an Appendix with page numbers and refer to it in the main document.

**Presentations:**

*You are required* to give three presentations on this design project. Each presentation will be followed by a 5-minute question and answer period. You will meet with the instructor to discuss the proposal and midterm presentations.

- 1) ***Proposal (Lab Time: Monday, Feb. 6, 2012):*** *You are required* to give a 10-minute project proposal presentation which lays out your intended design based on the given requirements and a workplan with specific tasks outlined. Include preliminary PSPICE schematics, timeline, initial parts list and budget.
- 2) ***Midterm Progress (Lab Time: Monday, March 19, 2012):*** *You are required* to give a 15-minute midterm progress report presentation which presents your progress up to the time of the presentation. Include PSPICE and CAD schematics and documented design results.
- 3) ***Final Report (Final Exam Time: Wednesday, May 9, 2012):*** *You are required* to give a 20-minute final project presentation explaining your design based on the given requirements and making final conclusions. Include PSPICE and CADENCE schematics and data/plots that support the final design requirements. The final presentations will occur during the scheduled Final Exam time which is Wednesday, May 9, 2012 from 10:15AM-12:15PM.

**Presentation Format:**

The specific presentation format requirements and evaluation criteria for the three oral presentations (proposal, midterm & final) will be provided in separate documents. The presentation outlines (Powerpoint slides) should include the sections of the respective written report. The slides should be readable and figures should be a high enough resolution to be readable from the audience. Strict adherence to the allotted time will be maintained, therefore, I strongly urge you to practice and time your presentations. Each presentation will be followed by a brief question and answer period and instructor/peer evaluation.

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**Grading (Points and percentage of class total):**

<b>Project Reports</b>			
	<b>Proposal</b>	<b>50</b>	<b>4%</b>
	<b>Bi-Weekly Progress (4)</b>	<b>50</b>	<b>4%</b>
	<b>Mid Term Progress</b>	<b>50</b>	<b>4%</b>
	<b>Draft Report</b>	<b>150</b>	<b>12%</b>
	<b>Final Report</b>	<b>125</b>	<b>10%</b>
<b>Lab Reports (6)</b>		<b>150</b>	<b>12%</b>
<b>Oral Presentations</b>			
	<b>Proposal</b>	<b>50</b>	<b>4%</b>
	<b>Mid Term Progress</b>	<b>50</b>	<b>4%</b>
	<b>Final</b>	<b>100</b>	<b>8%</b>
<b>Project Performance</b>			
	<b>Design Criteria</b>	<b>250</b>	<b>20%</b>
<b>Total Project Points</b>		<b>1025</b>	<b>82%</b>