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See <http://www.uaf.edu/uafgov/faculty/cd> for a complete description of the rules governing curriculum & course changes.

CHANGE COURSE (MAJOR) and DROP COURSE PROPOSAL

SUBMITTED BY:

Department	Electrical & Comp Engr	College/School	CEM
Prepared by	Richard Wies	Phone	474-7071
Email Contact	rwwiesjr@alaska.edu	Faculty Contact	Richard Wies

1. COURSE IDENTIFICATION:

Dept Course # No. of Credits

COURSE TITLE

2. ACTION DESIRED:

Change Course ☒ If Change, indicate below what change. Drop Course ☐

NUMBER		TITLE	☒	DESCRIPTION	☒
PREQUISITES	☒			FREQUENCY OF OFFERING	
CREDITS (including credit distribution)	4 (3+3)			COURSE CLASSIFICATION	☒
CROSS-LISTED		Dept.		(Requires approval of both departments and deans involved. Add lines at end of form for such signatures.)	
STACKED (400/600)		Dept.		Course #	
Include syllabi.					
OTHER (please specify)					

3. COURSE FORMAT

NOTE: Course hours may not be compressed into fewer than three days per credit. Any course compressed into fewer than six weeks must be approved by the college or school's curriculum council. Furthermore, any core course compressed to less than six weeks must be approved by the core review committee.

COURSE FORMAT: (check all that apply) ☐ 1 ☐ 2 ☐ 3 ☐ 4 ☐ 5 ☒ 6 weeks to full semester

OTHER FORMAT (specify all that apply)

Mode of delivery (specify lecture, field trips, labs, etc)

4. COURSE CLASSIFICATIONS: (undergraduate courses only. Use approved criteria found on Page 10 & 17 of the manual. If justification is needed, attach on separate sheet.)

H = Humanities ☐

S = Social Sciences ☐

Will this course be used to fulfill a requirement for the baccalaureate core?

YES ☒

NO ☐

IF YES, check which core requirements it could be used to fulfill:

O = Oral Intensive, Format 6 also submitted ☒

W = Writing Intensive, Format 7 submitted ☒

Natural Science, Format 8 submitted ☐

5. COURSE REPEATABILITY:

Is this course repeatable for credit?

YES ☐

NO ☒

Justification: Indicate why the course can be repeated (for example, the course follows a different theme each time).

How many times may the course be repeated for credit?

TIMES

If the course can be repeated with variable credit, what is the maximum number of credit hours that may be earned for this course?

CREDITS

6. **CURRENT CATALOG DESCRIPTION AS IT APPEARS IN THE CATALOG:** including dept., number, title and credits

EE F408 Power Electronics

3 Credits Offered Spring

Study of past and current technology used in power conversion and control equipment. Topics will include the theory and application of thyristors, rectifiers, DC-DC converters, inverters, resonant converters, AC and DC switches and regulators, power supplies, DC drives, and adjustable-speed drives including variable-frequency drives and cycloconverters. *Prerequisites: EE F303; EE F333; or permission of instructor.* Stacked with EE F608. (3+0)

7. **COMPLETE CATALOG DESCRIPTION AS IT WILL APPEAR WITH THESE CHANGES:** (Underline new wording strike through old wording and use complete catalog format including dept., number, title, credits and cross-listed and stacked.) **PLEASE SUBMIT NEW COURSE SYLLABUS.** For stacked courses the syllabus must clearly indicate differences in required work and evaluation for students at different levels.

EE F408 Power Electronics Design if W"

4 Credits Offered Spring

~~Study of past and current technology used in power conversion and control equipment.~~ Analysis and design of power electronic conversion, control and drive systems. Topics will include the theory and application of thyristors, rectifiers, DC-DC converters, inverters, resonant converters, AC and DC switches and regulators, power supplies, DC drives, and adjustable-speed drives including variable-frequency drives ~~and cycloconverters.~~ Includes laboratory exercises using power electronic converter boards, PSPICE, and a complete power electronics design project. *Prerequisites: COMM F131X or COMM F141X; EE 303; EE 334; EE 354; ENGL F111X; ENGL F211X or ENGL 213X or permission of instructor; senior standing.* Stacked with EE F608. (3+3)

8. **IS THIS COURSE CURRENTLY CROSS-LISTED?**

YES/NO

☒ NO

If Yes, DEPT

NUMBER

(Requires written notification of each department and dean involved. Attach a copy of written notification.)

9. **GRADING SYSTEM:** Specify only one

LETTER:

☒ X

PASS/FAIL:

☐

10. **ESTIMATED IMPACT**

WHAT IMPACT, IF ANY, WILL THIS HAVE ON BUDGET, FACILITIES/SPACE, FACULTY, ETC.

There will be no additional impact on budget, facilities, and faculty due to this course change. The course will require a classroom and be a part of the instructor's normal faculty teaching workload and a laboratory with experiments is already set up for this course. The course will also satisfy the undergraduate senior design requirement within EE and also be available for graduate credit, therefore, it optimizes budget, facilities, and faculty resources.

11. **LIBRARY COLLECTIONS**

Have you contacted the library collection development officer (kljensen@alaska.edu, 474-6695) with regard to the adequacy of library/media collections, equipment, and services available for the proposed course? If so, give date of contact and resolution. If not, explain why not.

No

☐

Yes

☒ X

I called Karen on Tuesday, Sept. 6, 2011 and verified that the library has the appropriate electronic journal transactions: IEEE Transactions on Power Electronics and other reference material required for this course.

12. **IMPACTS ON PROGRAMS/DEPTS:**

What programs/departments will be affected by this proposed action?

Include information on the Programs/Departments contacted (e.g., email, memo)

The course change affects the Electrical and Computer Engineering Department and the BSEE program.

13. **POSITIVE AND NEGATIVE IMPACTS**

Please specify **positive and negative** impacts on other courses, programs and departments resulting from the proposed action.

Positive: The course provides a SENIOR DESIGN ELECTIVE which SATISFIES the UNDERGRADUATE SENIOR DESIGN REQUIREMENT and the W/O intensive course requirement for EE undergraduates in the power and controls option.

JUSTIFICATION FOR ACTION REQUESTED

The purpose of the department and campus-wide curriculum committees is to scrutinize course change and new course applications to make sure that the quality of UAF education is not lowered as a result of the proposed change. Please address this in your response. This section needs to be self-explanatory. If you ask for a change in # of credits, explain why; are you increasing the amount of material covered in the class? If you drop a prerequisite, is it because the material is covered elsewhere? If course is changing to stacked (400/600), explain higher level of effort and performance required on part of students earning graduate credit. Use as much space as needed to fully justify the proposed change and explain what has been done to ensure that the quality of the course is not compromised as a result.

A course in power electronics design is needed to supplement the electrical engineering undergraduate senior design elective requirements for the power and controls option and student interest in emerging technology associated with power electronic devices and their use in modern electric power systems. Although the lecture portion of this course is similar to the current EE 408/608: Power Electronics course, the course change adds a SENIOR DESIGN ELECTIVE component through the addition of a laboratory and senior design project which SATISFIES the UNDERGRADUATE SENIOR DESIGN REQUIREMENT and the W/O intensive course requirement for EE undergraduates in the power and controls option.

APPROVALS:

Signature, Chair, Program/Department of:	Date
Charles E. Mayer	9/27/11

Signature, Chair, College/School Curriculum Council for: <i>Neelamita Misra</i>	Date: <i>9/30/11</i>
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Signature, Dean, College/School of: AA Date: 10/3/11

Signature of Provost (if applicable) Date 16/6/11

Offerings above the level of approved programs must be approved in advance by the Provost.

ALL SIGNATURES MUST BE OBTAINED PRIOR TO SUBMISSION TO THE GOVERNANCE OFFICE.

Signature, Chair, UAF Faculty Senate Curriculum Review Committee _____ Date _____

ADDITIONAL SIGNATURES: (As needed for cross-listing and/or stacking)

	Date	
Signature, Chair, Program/Department of:		

	Date	
Signature, Chair, College/School Curriculum Council for:		

	Date	
Signature, Dean, College/School of:		

ATTACH COMPLETE SYLLABUS (as part of this application).

Note: The guidelines are online: <http://www.uaf.edu/uafgov/faculty/cd/syllabus.html>

The department and campus wide curriculum committees will review the syllabus to ensure that each of the items listed below are included. If items are missing or unclear, the proposed course change will be denied.

SYLLABUS CHECKLIST FOR ALL UAF COURSES

During the first week of class, instructors will distribute a course syllabus. Although modifications may be made throughout the semester, this document will contain the following information (as applicable to the discipline):

1. Course information:

☐ Title, ☐ number, ☐ credits, ☐ prerequisites, ☐ location, ☐ meeting time
(make sure that contact hours are in line with credits).

2. Instructor (and if applicable, Teaching Assistant) information:

☐ Name, ☐ office location, ☐ office hours, ☐ telephone, ☐ email address.

3. Course readings/materials:

☐ Course textbook title, ☐ author, ☐ edition/publisher.
☐ Supplementary readings (indicate whether ☐ required or ☐ recommended) and
☐ any supplies required.

4. Course description:

☐ Content of the course and how it fits into the broader curriculum;
☐ Expected proficiencies required to undertake the course, if applicable.
☐ Inclusion of catalog description is *strongly* recommended, and
☐ Description in syllabus must be consistent with catalog course description.

5. ☐ Course Goals (general), and (see #6)

6. ☐ Student Learning Outcomes (more specific)

7. Instructional methods:

☐ Describe the teaching techniques (eg: lecture, case study, small group discussion, private instruction, studio instruction, values clarification, games, journal writing, use of Blackboard, audio/video conferencing, etc.).

8. Course calendar:

☐ A schedule of class topics and assignments must be included. Be specific so that it is clear that the instructor has thought this through and will not be making it up on the fly (e.g. it is not adequate to say "lab". Instead, give each lab a title that describes its content). You may call the outline Tentative or Work in Progress to allow for modifications during the semester.

9. Course policies:

☐ Specify course rules, including your policies on attendance, tardiness, class participation, make-up exams, and plagiarism/academic integrity.

10. Evaluation:

☐ Specify how students will be evaluated, ☐ what factors will be included, ☐ their relative value, and
☐ how they will be tabulated into grades (on a curve, absolute scores, etc.)

11. Support Services:

☐ Describe the student support services such as tutoring (local and/or regional) appropriate for the course.

12. Disabilities Services:

The Office of Disability Services implements the Americans with Disabilities Act (ADA), and insures that UAF students have equal access to the campus and course materials.

☐ State that you will work with the Office of Disabilities Services (208 WHIT, 474-5655) to provide reasonable accommodation to students with disabilities."

**University of Alaska Fairbanks
Electrical and Computer Engineering Department
EE 408/608 - Power Electronics Design
Spring 2012**

Course Information:

Title: EE 408/608 Power Electronics Design (3+3)
Lecture Time: MWF (11:45AM-12:45PM) in Duckering 202
Lab Time: M (2:15-5:15PM) in Duckering 202, Duckering 330, and Duckering 216
Prerequisites: COMM F131X or COMM F141X; EE 303; EE 334; EE 354; ENGL F111X; ENGL F211X or ENGL 213X or permission of instructor; senior standing

Instructor: Dr. Richard Wies, Associate Professor, ECE Dept.
Office: Duckering 213
Office Hours: W 2-3PM, TR 11AM-12PM or by phone/e-mail
Phone: 474-7071
E-mail: rwwiesjr@alaska.edu

Required Text: Mohan, Undeland, and Robbins, Power Electronics: Converters, Applications, and Design, 3rd ed., Wiley, 2003.

References: Daniel W. Hart, Power Electronics, McGraw-Hill, 2011.

Course Description: Analysis and design of power electronic conversion, control and drive systems with emphasis on smart grid applications. Topics will include the theory and application of thyristors, rectifiers, DC-DC converters, inverters, resonant converters, AC and DC switches and regulators, power supplies, DC drives, and adjustable-speed drives. Includes laboratory exercises using power electronic converter boards and a complete power electronics design project.

Course Goals: Students will develop an understanding of power electronic conversion, control and drive systems with emphasis on analysis and design concepts. The course will develop the building blocks for power electronic devices including rectifiers, inverters, and converters. Analysis will include the use of PSPICE and the use of Fourier transforms for harmonics. Design will include a project to build an operational power electronic conversion device.

Instruction Methods: Application of fundamental circuit and electronic principles, including time domain and Fourier analysis, in the analysis, design and operation of power electronic devices.

Evaluation/Grading: Plus/Minus grading will be used – see page 48 of the 2011-2012 UAF catalog for numerical values. Grades will be assigned based on absolute scores.

Written and Oral Intensive Course (W/O) Requirements: EE 4/608 Power Electronic Design is a writing and oral intensive course and according to university regulations 50% of the course grade must be based on written work and 15% of the course grade must be based on oral presentations. The grade for written assignments will be based on content, English usage, organization and format. Content refers to the effectiveness of the report in communicating the technical aspects of the assignment. English usage refers to punctuation, spelling, sentence structure, etc. Organization refers to how logically the sentences, paragraphs and body of the report are organized. Format refers to how well the report conforms to the required report formatting guidelines. A personal interview with each student will follow the completion of the rough draft of the final report to provide feedback for refinement of the final report.

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Oral presentations will be graded according to content, voice, use of visual aides and format. There will be three presentations of at least 10 minutes each and one impromptu presentation. Each presentation will be followed by a brief question and answer period and instructor evaluation.

The percentages in the following grading criteria are based on the total points possible (1000) in the course.

Exam I	75 (7.5%)
Exam II	75 (7.5%)
Written/Oral	
Written	500 (50%)
Project Reports	
Proposal	5%
Mid Term Progress	5%
Draft Report	15%
Final Report	10%
Lab Reports	15%
Oral Presentations	150 (15%)
Proposal	3%
Mid Term Progress	3%
Final	9%
Project Performance	150 (15%)
<u>Homework</u>	<u>50 (5%)</u>
Total	1000(100%)

408 vs 608: Students enrolled in 608 will be required to complete a design project that involves 2 times the workload for the undergraduates. Graduate students will also be required to complete more difficult questions on exams and homework. The questions will be optional for undergraduate students who may complete them for extra credit.

Course Schedule/Senior Design Components:

The course schedule attached to this syllabus provides topics for each lecture and laboratory. Note that a significant portion of the laboratory is centered around the senior design project. The specific design project components of the lecture and laboratory in this course are highlighted in green in the schedule, including a proposal, three project reports, three oral presentations, and project demonstrations (performance). A senior project design requirement and specification handout will be provided during the first laboratory session.

Course Policies:

Homework: Homework will be assigned on a weekly basis and is due at the beginning of class on the due date. No late homework will be accepted unless previously authorized by the instructor. I encourage discussion of solution techniques with your fellow students, but you must independently formulate your results. Some homework assignments will include computer-aided simulation and analysis of power electronic devices and systems. Homework solutions will be posted on Blackboard within a few days after the assignments are due.

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Exams: Midterm exams are open book and closed notes with *two* 8.5x11 (INCHES) formula sheets allowed. Formula sheets cannot have solved problems and must be attached to the exam. **Laptops, cell phones, and calculators with communication capability (Bluetooth, etc.) are not permitted to be used during the exams.** Absences from exams must be preceded by a valid excuse. In the event of a valid excused absence it is the student's responsibility to contact the instructor to arrange for a make-up exam.

Cheating/Plagiarism: Cheating and plagiarism will not be tolerated and will result in failure of the course.

Attendance: As directly quoted from the 2011-2012 UAF Course Catalog under Academics/Attendance (page 48):

"You are expected to attend class regularly; unexcused absences may result in a failing grade. You are responsible for conferring with your instructor concerning absences and the possibility of arranging to make up missed work....However, your instructor is under no obligation to allow you to make up missed work for unexcused absences or if notification and arrangements are not made in advance of the absence."

Disabilities Services: The Office of Disability Services implements the Americans with Disabilities Act (ADA), and insures that UAF students have equal access to the campus and course materials. The instructor, the teaching assistant, and the administrative assistant will work with the Office of Disabilities Services to provide reasonable accommodation to students with disabilities. Disability Services is located at the Center for Health and Counseling in 203 WHIT. The coordinator of Disability Services can be contacted by phone at 474-7043 or 474-7045 (TTY), and by email at fydso@uaf.edu.

Student Learning Outcomes:

The B.S.E.E. program at UAF is accredited by the Accreditation Board for Engineering and Technology (ABET). Accreditation requires that all students graduating from this program must achieve the following Program Outcomes. This course addresses the Program Outcomes indicated below in bold:

- (a) **an ability to apply knowledge of mathematics, science, and engineering**
- (b) **an ability to design and conduct experiments, as well as to analyze and interpret data**
- (c) **an ability to design a system, component, or process to meet desired needs**
- (d) **an ability to function on multi-disciplinary teams**
- (e) **an ability to identify, formulate, and solve engineering problems**
- (f) **an understanding of professional and ethical responsibility**
- (g) **an ability to communicate effectively**
- (h) **the broad education necessary to understand the impact of engineering solutions in a global and societal context**
- (i) **a recognition of the need for, and an ability to engage in life-long learning**
- (j) **a knowledge of contemporary issues**
- (k) **an ability to use the techniques, skills, and modern engineering tools necessary for engineering practice**

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EE 408/608 – Tentative Lecture/Lab Schedule – Spring 2012

All dates and topics are tentative. Exam dates are subject to change.

Topic/Lecture/Lab	Topic/Lecture/Lab	Topic/Lecture/Lab	Topic/Lecture/Lab
			Jan. 20 – Lecture #1 Power Electronics: Introduction; Compared to Linear Electronics; Classification of Power Processors; Convention – Sections 1.1-1.7
Jan. 23 – Lecture #2 Semiconductor Switching Devices: Available Types, Characteristics, Functions, Comparison & Future Trends; Snubber Circuits – Sections 2.1-2.11	Jan. 23 – Lab #1 Introduction to Project – Project Overview – Presentation Guideline – Project Proposal – Break into Teams	Jan. 25 – Lecture #3 Power Concepts: Displacement pf (DPF); Harmonics: Fourier Analysis, Line Current Harmonics, THD, and True PF – Section 3.2.4	Jan. 27 – Lecture #4 Inductor & Capacitor Response: Steady-State and Transient Responses – Sections 3.2.5
Jan. 30 – Lecture #5 DC-DC Switch Mode Converters: Types; PWM Control; Buck (Step- Down) with Continuous Conduction Mode (CCM) – Sections 7.1-7.3.2	Jan. 30 – Lab #2 Introduction to: – PSpice Power Electronics Tools – Power Pole DC Converter Experiment Boards – TTech J5 Circuit Board Prototyping Machine	Feb. 1 – Lecture #6 DC-DC Switch Mode Converters: Buck with & Discontinuous Conduction Mode (DCM); Output Voltage Ripple; Boost (Step-Up) with CCM & DCM – Section 7.3.3-7.4	Feb. 3 – Make-Up Lecture
Feb. 6 – Lecture #7 DC-DC Switch Mode Converters: Buck-Boost with CCM & DCM – Section 7.5	Feb. 6 – Lab #3 Oral Presentation I: Project Proposal Presentations – Project Proposals Due	Feb. 8 – Lecture #8 DC-DC Switch Mode Converters: Cuk – Section 7.6	Feb. 10 – Lecture #9 DC-DC Switch Mode Converters: Full-Bridge (4-quadrant); Bipolar and Unipolar Switching; Voltage Ripple – Section 7.7
Feb. 13 – Lecture #10 DC-DC Switch Mode Converters: Comparison using Switch Utilization Factor; Equivalent Circuits; Reversing Power Flow – Sections 7.8	Feb. 13 – Lab #4 DC Converters: Buck, Boost, and Buck-Boost Converter using the Power Pole Board	Feb. 15 – Lecture #11 Uncontrolled Diode Rectifiers: Concept using simple R-L Circuit and Back EMF; Single-Phase Diode Bridge Rectifier Circuits with DC Side Current Source – Sections 5.1-5.3	Feb. 17 – Lecture #12 Uncontrolled Diode Rectifiers: Single-Phase Diode Bridge Rectifier Circuits with: 1) AC side Inductance and DC Side Current Source 2) AC Side Inductance and DC Side Voltage – Section 5.3

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MONDAY (Lecture)	TUESDAY (Lab)	WEDNESDAY	THURSDAY
Feb. 20 – Lecture #13 Uncontrolled Diode Rectifiers: Single-Phase Diode Bridge Rectifier Line Current and Voltage Distortion; Voltage Doubler; Three-Phase, 4-Wire Neutral Current Effects – Sections 5.3-5.5	Feb. 20 – Lab #5 DC Converters: Flyback and Forward Converters using the Power Pole Boards	Feb. 22 – Lecture #14 Uncontrolled Diode Rectifiers: Three-Phase, Full-Bridge Rectifier – Section 5.6	Feb. 24 – Lecture #15 Uncontrolled Diode Rectifiers: Comparison of Single-Phase and Three-Phase; Inrush Current and Overvoltage at Turn-On; Improving Line Current Harmonics and Power Factor – Sections 5.7-5.9
Feb. 27 – Lecture #16 Phase-Controlled Rectifiers: Thyristor Circuits – Sections 6.1-6.2	Feb. 27 – Lab #6 Switching Characteristics of MOSFETs and DIODEs used in DC Converters	Feb. 29 – Review for EXAM #1 Ch. 1-4, & 7 OPEN BOOK 2 Formulas Sheets	Mar. 2 – EXAM #1 Ch. 1-4, & 7 OPEN BOOK 2 Formulas Sheets
Mar. 5 – Lecture #17 Phase-Controlled Rectifiers: Ideal Single-Phase Converters – Section 6.3.1	Mar. 5 – Lab #7 Voltage and Peak Current Mode Control of DC Converters	Mar. 7 – Lecture #18 Phase-Controlled Rectifiers: Single-Phase Converters with Source Inductance – Section 6.3.2	Mar. 9 – Lecture #19 Phase-Controlled Rectifiers: Practical Single-Phase Converters and Inverter Mode of Operation – Sections 6.3.3-6.3.4
Mar. 12 – NO CLASS Spring Break	Mar. 12 – NO LAB Spring Break	Mar. 14 – NO CLASS Spring Break	Mar. 16 – NO CLASS Spring Break
Mar. 19 – Lecture #20 Phase-Controlled Rectifiers: Ideal Three-Phase Converters – Sections 6.4.1	Mar. 19 – Lab #8 Oral Presentation II: Progress Report Presentations - Project Progress Reports Due	Mar. 21 – Lecture #21 Phase-Controlled Rectifiers: Three-Phase Converters with Source Inductance – Sections 6.4.2	Mar. 23 – Lecture #22 Phase-Controlled Rectifiers: Practical Three-Phase Converters and Inverter Mode of Operation – Sections 6.4.3-6.4.4
Mar. 26 – Lecture #23 Switch-Mode Inverters: Basic Concept, PWM, & Square-Wave Switching – Section 8.2	Mar. 26 – Lab #9 Single-Phase Diode Rectifiers: Design and Simulation	Mar. 28 – Lecture #24 Switch-Mode Inverters: Single-Phase Half-Bridge; Full-Bridge with Bipolar Switching – Sections 8.3.1-8.3.2.1	Mar. 30 – Lecture #25 Switch-Mode Inverters: Single-Phase Full-Bridge with Unipolar Switching & Square Wave Mode of Operation – Sections 8.3.2.2-8.3.2.3

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DATE	TOPIC	TOPIC	TOPIC
Apr. 2 – Lecture #26 Switch-Mode Inverters: Single-Phase Full-Bridge with Voltage Cancellation; Switch Utilization; Voltage Output Ripple – Sections 8.3.2.4-8.3.2.6	Apr. 2 – Lab #10 Design Project Time	Apr. 4 – Lecture #27 Switch-Mode Inverters: Push-Pull Inverters; Switch Utilization – Sections 8.3.3-8.3.4	Apr. 6 – Lecture #28 Switch-Mode Inverters: Three-Phase Inverters and Voltage Source PWM – Sections 8.4.1
Apr. 9 – Lecture #29 Switch-Mode Inverters: Three-Phase Inverters with Square-Wave Operation; Switch Utilization; Voltage Output Ripple – Sections 8.4.2-8.4.4	Apr. 9 – Lab #11 Design Project Time	Apr. 11 – Lecture #30 Switch-Mode Inverters: Three-Phase Inverters with DC side Current; Switch Conduction – Sections 8.4.5-8.4.6	Apr. 13 – Lecture #31 Switch-Mode Inverters: Effect of Blanking Time on Voltage in PWM Inverters – Section 8.5
Apr. 16 – Lecture #32 Switch-Mode Inverters: Other Switching Schemes; Rectifier Mode of Operation – Sections 8.6-8.7	Apr. 16 – Lab #12 Design Project Time	Apr. 18 – Review for EXAM #2 Ch. 5-6, & 8 OPEN BOOK 2 Formulas Sheets	Apr. 20 – EXAM #2 Ch. 5-6, & 8 OPEN BOOK 2 Formulas Sheets
Apr. 23 – Lecture #33 Selected Topics in Power Electronic Applications: Power Supplies (Switching dc, Power Conditioning, UPS) – Ch. 11	Apr. 23 – Lab #13 Design Project Time - Draft Project Reports Due	Apr. 25 – Lecture #34 Selected Topics in Power Electronic Applications: Motor Drives (dc, ac Induction & Synchronous) – Ch. 12-15	Apr. 27 – NO CLASS Nanook SpringFest
Apr. 30 – Lecture #35 Selected Topics in Power Electronic Applications: Residential/Industrial – Ch. 16	Apr. 30 – Project Evaluations (Demonstrations)	May 2 – Lecture #36 Selected Topics in Power Electronic Applications: Utility (HVDC, SVC, and Grid Connected Renewables) – Ch. 17	May 4 – Lecture #37 Selected Topics in Power Electronic Applications: Optimizing the Utility Interface (Smart Grid) – Ch. 18
		May 11 – FINAL EXAM 10:15 AM – 12:15 PM FINAL PROJECT PRESENTATIONS - Final Project Reports Due	

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Senior/Graduate Design Project: Regulated DC Hard Drive Bay Power Supply Design

Objective:

The objective of this project is to design, simulate using PSPICE, and build a regulated dc power supply fed from a single phase AC source that meets the design requirements listed below. You will need to combine what you learn in the class lecture and laboratory and additional literature such as technical articles and/or materials from the textbook to complete this project.

Design Problem Statement:

You work for WIES Power Electronics Design and your boss has asked you to design a regulated dc source to power a hard drive bay in a server to store power systems data that is being uploaded from remote monitoring sites in 20 remote communities in Alaska. Each community has a single 1TB hard drive in the bay. Each 1TB drive requires a constant 12 Vdc to operate the motor and a constant 5Vdc to operate the drive electronics with a typical average power consumption model $P_{typ} = (\text{Idle} * 90\% + \text{Write} * 2.5\% + \text{Read} * 7.5\%) / 100\%$ and a maximum average power consumption model $P_{max} = (\text{Write} + \text{Seek} + \text{Read} * 3) / 5$ with idle, write, read, and seek powers to be provided. The available supply is single phase 120 Vac +/- 5% at 60 Hz with an unknown supply inductance L_s . The use of a isolation transformer is required in the design and the leakage reactance must be less than 10% based on its ratings.

Design Requirements (Graduate Credit in BOLD):

- 1) The dc voltages applied to the hard drive motor and drive electronics must remain constant at 12 Vdc and 5 Vdc, respectively.
- 2) The proper amount of additional supply inductance must be employed to meet the German VDE standards (inductive reactance is a minimum of 5% of the base impedance).
- 3) An isolation transformer is required in the design and the leakage reactance must be less than 10% based on its ratings.
- 4) The THD_i of the supply current for this system must be less than 20%.
- 5) The THD_v of the voltage at the point of common coupling (PCC) must be less than 10%.
- 6) The percent voltage ripple at the output of the dc-dc converter must be less than 5%.
- 7) Simulate the design in PSPICE before construction of the dc regulated power supply.
- 8) **The power supply must operate from a single phase 120 Vac +/- 5% at 60 Hz or single phase 250 Vac +/- 5% at 50Hz supply with unknown supply inductance L_s .**
- 9) **The power supply must provide power to operate two 9 Vdc & 2 Adc cooling fans.**

Performance Verifications Plots/Calculations Requirements:

- 1) Simulate the design in PSPICE. Is the design possible with the given requirements? If not, which requirement(s) cannot be achieved? Why not?
 - a. Plot the waveforms v_s , i_s , v_d , i_d , and v_{PCC} of the diode rectifier on one plot using PSPICE. Use the supply side (primary) of the transformer as the point of common coupling (PCC).
 - b. Use Fourier analysis in PSPICE to compute I_{st} , I_s , DPF, PF, and THD_i .
 - c. Also compute the THD_v of the voltage at the point of common coupling (PCC).
 - d. Plot the waveforms v_d , i_d , v_o , and i_o of the dc-dc converter using PSPICE.
 - e. Calculate the peak-to-peak and percent voltage ripple in v_d and v_o .
- 2) Verify all design requirements on completed power supply.

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Senior/Graduate Design Project: Regulated DC Hard Drive Bay Power Supply Design

Team Approach:

You will be working in teams of two on this design project and each of the team members will have specific tasks that will be documented in a project workplan as part of the proposal. The instructor will sign off on each task in the workplan as it is completed.

Reports:

You are required to write four reports as part of the design project

- 1) **Proposal (Due: Monday, Feb. 7, 2010):** *You are required* to write a project proposal (5 pages or less) which lays out your intended design based on the given requirements. Include preliminary PSPICE schematics and necessary output text files. *You will be required* to submit a project workplan outlining the tasks for each team member as part of the proposal. You will meet with the instructor to discuss the proposal.
- 2) **Midterm Progress (Due: Monday, March 2, 2010):** *You are required* to write a midterm progress report (5 pages or less) which discusses your progress up to the time of the mid-term progress presentations. You will meet with the instructor to discuss the mid-term progress.
- 3) **Draft Report (Due: Monday, April 25, 2010):** *You are required* to write a draft project report (approximately 20 pages) explaining your design based on the given requirements. Include PSPICE and CADENCE schematics and necessary output text files in an Appendix. Also make sure to include any references and annotate them in order of reference within the report. You will meet with the instructor to discuss edits to the draft report.
- 4) **Final Report (Due: Wednesday, May 11, 2010):** *You are required* to complete a final project report (approximately 20 pages) explaining your design based on the given requirements and making final conclusions and edits from the draft report. Include PSPICE and CADENCE schematics and necessary output text files in an Appendix. Also make sure to include any references and annotate them in order of reference within the report.

Report Format:

The proposal and reports should be sufficient in length to cover the theory, design, and simulation. The reports should give background on the specific application and explain how it applies to power electronics. Your report should also include any pertinent tables, figures, PSPICE inputs and outputs and CADENCE schematics for the purpose of demonstrating the application. This should be written like a standard research report with references. Please provide a list of numbered references (including websites) in the order referenced in the report and use the numbers in square brackets to annotate the referenced material in the report. Figures and tables need to have captions. Captions for figures are below the figure, while captions for tables are above the table. Also, please number the pages beginning with the second page. If you have an excessive amount of design and simulation results, you should include it in an Appendix with page numbers and refer to it in the main document.

**University of Alaska Fairbanks
Electrical and Computer Engineering Department
EE 408/608 – Power Electronics Design
Spring 2012**

Senior/Graduate Design Project: Regulated DC Hard Drive Bay Power Supply Design

Presentations:

You are required to give three team presentations on this design project:

- 1) **Proposal (Lab Time: Monday, Feb. 7, 2010):** *You are required* to give a 15 minute project proposal presentation which lays out your intended design based on the given requirements and a workplan with tasks outlined for both team members. Include preliminary PSPICE schematics.
- 2) **Midterm Progress (Lab Time: Monday, March 2, 2010):** *You are required* to give a 15 minute midterm progress report presentation which presents your progress up to the time of the presentation. Include PSPICE and CADENCE schematics and any documented design results.
- 3) **Final Report (Final Exam Time: Wednesday, May 11, 2010):** *You are required* to give a 20-minute final project presentation explaining your design based on the given requirements and making final conclusions. Include PSPICE and CADENCE schematics and data/plots that support the final design requirements. The final presentations will occur during the scheduled Final Exam time which is Wednesday, May 11, 2011 from 10:15AM-12:15PM.

Grading:

Project Reports	
Proposal	50
Mid Term Progress	50
Draft Report	150
Final Report	100
Oral Presentations	
Proposal	30
Mid Term Progress	30
Final	90
<u>Project Performance</u>	<u>150</u>
Total Project	650